Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S2	2	2003/0002596 2003/0002607 2003/0122588 2003/0123589 2003/0123594	US-PGPUB; USPAT	OR	OFF	2006/03/30 14:13
S1	1	"20050251710"	US-PGPUB; USPAT	OR	OFF	2006/03/30 14:13
S3	5	"20030002596" "20030002607" "20030122588" "20030123589" "20030123594"	US-PGPUB; USPAT	OR	OFF	2006/03/30 14:16
S4	3	10/383501 10/400957	US-PGPUB; USPAT	OR	OFF	2006/03/30 14:18
S5	17	(Tarango ADJ Tony Swartz ADJ Ronald).in.	US-PGPUB; USPAT	OR	OFF	2006/03/30 14:19
S8	3	(Tarango ADJ Tony Swartz ADJ Ronald).in. and phase with loop	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:20
S6	20	(Tarango ADJ Tony Swartz ADJ Ronald).in.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:20
S7	6	(Tarango ADJ Tony Swartz ADJ Ronald).in. and phase	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:21
S10	21	phase with loop and kerveros	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:25
S11	5	phase with loop and jitter\$3 and kerveros	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:36
S13	1	phase near lock\$3 near loop and jitter\$3 and offset and kerveros	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:39
S14	9851	phase near lock\$3 near loop and jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:43
S15	9828	phase adj lock\$3 adj loop and jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:44

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S18	231	"714"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:46
S19	16	"714"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3 and offset	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:50
S23	14	phase adj lock\$3 adj loop with jitter\$3 with data adj recover\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:58
S17	48	"714"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 14:58
S28	14	phase with loop with recovery with jitter\$3 and link and integrated adj circuit and transmi\$5 with receiv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:15
S27	34	phase with loop with recovery with jitter\$3 and integrated adj circuit and transmi\$5 with receiv\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:15
S30	70478	phase adj lock\$3 loop with recovery with jitter\$3 and link and integrated adj circuit and transmi\$5 with receiv\$3 and @ad<"20040330"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:17
S29	13	phase with loop with recovery with jitter\$3 and link and integrated adj circuit and transmi\$5 with receiv\$3 and @ad<"20040330"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:17
S31	11	phase adj lock\$3 adj loop with recovery with jitter\$3 and link and integrated adj circuit and transmi\$5 with receiv\$3 and @ad<"20040330"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:18
S22	6	"714"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 near3 offset	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:21
S32	5	"714"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 near3 simulat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:25
S33	56	phase adj lock\$3 adj loop and jitter\$3 near3 simulat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:26
S35	1	("6671863").PN.	USPAT	OR	OFF	2006/03/30 15:29

S34	4	phase adj lock\$3 adj loop with jitter\$3 near3 simulat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:30
S36	56	phase adj lock\$3 adj loop and jitter\$3 near3 simulat\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:36
S38	1	phase adj lock\$3 adj loop with jitter\$3 near3 phase adj offset	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:37
S37	34	phase adj lock\$3 adj loop and jitter\$3 near3 phase adj offset	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:37
S40	2	"20050135501"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:42
S39	4	phase adj lock\$3 adj loop same jitter\$3 near3 phase adj offset	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:42
S41	2	("3737766" "5900755").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/30 15:47
S12	5	phase near lock\$3 near loop and jitter\$3 and kerveros	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:57
S43	3897	phase adj lock\$3 adj loop and jitter\$3 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 15:59
S45	655	phase adj lock\$3 adj loop and jitter\$3 and data adj recover\$3 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 16:00
S44	3	phase adj lock\$3 adj loop and jitter\$3 and data adj recover\$3 and integrated adj circuit with under adj test	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 16:00
S46	89	phase adj lock\$3 adj loop with jitter\$3 and data adj recover\$3 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/30 16:01

25	phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR .	ON	2006/03/30 16:02
1	"6331999".PN.	USPAT; USOCR	OR	OFF	2006/03/30 17:34
1	"4809306".PN.	USPAT; USOCR	OR	OFF	2006/03/30 17:34
1	"324"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3	USPAT	OR	ON	2006/03/30 17:43
12	phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 and integrated adj circuit	USPAT	OR	ON	2006/03/30 17:43
8	"324"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6)	USPAT	OR	ON	2006/03/30 17:50
3	"324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 with phase adj (offset adjust\$5 var\$6)	USPAT	OR	ON	2006/03/30 17:57
5	"324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 same phase adj (offset adjust\$5 var\$6)	USPAT	OR	ON ·	2006/03/30 17:58
4	"324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) and "IC"	USPAT	OR	ON	2006/03/30 18:01
28	"324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6)	USPAT	OR '	ON	2006/03/30 18:01
57	phase with loop with recovery with jitter\$3 and integrated adj circuit	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/04/03 14:54
800	"714"/\$.ccls. and scan adj chain	USPAT	OR	OFF	2006/04/03 16:35
24	"714"/\$.ccls. and scan adj chain with segment\$4	USPAT	OR	OFF	2006/04/03 16:37
2	"714"/\$.ccls. and chain with segment\$4 with fault	USPAT	OR	ON	2006/04/03 16:39
1	"714"/\$.ccls. and scan adj chain with segment\$4 with fault	USPAT	OR	ON	2006/04/03 16:39
10	"714"/\$.ccls. and scan with segment\$4 same fault	USPAT	OR	ON	2006/04/03 17:00
9	"714"/\$.ccls. and scan adj chain with (segment\$4 partition\$3) same (fault error)	USPAT	OR	ON	2006/04/03 17:01
	1 1 1 1 12 8 8 3 5 4 28 57 800 24 2 1 10	jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 and integrated adj circuit "6331999".PN. "4809306".PN. "324"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 and integrated adj circuit "324"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 with phase adj (offset adjust\$5 var\$6) "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 same phase adj (offset adjust\$5 var\$6) "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 same phase adj (offset adjust\$5 var\$6) "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) "324"/\$.ccls. and scan adj chain "C" "324"/\$.ccls. and scan adj chain with segment\$4 "714"/\$.ccls. and scan adj chain with segment\$4 with fault "714"/\$.ccls. and scan adj chain with segment\$4 with fault "714"/\$.ccls. and scan adj chain with segment\$4 with fault "714"/\$.ccls. and scan adj chain with segment\$4 with fault "714"/\$.ccls. and scan adj chain with segment\$4 same fault "714"/\$.ccls. and scan adj chain with segment\$4 with fault "714"/\$.ccls. and scan adj chain with segment\$4 with fault	jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 and integrated adj circuit 1 "6331999".PN. 1 "4809306".PN. 1 "324"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 12 phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 and integrated adj circuit 8 "324"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) 3 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 5 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 same phase adj (offset adjust\$5 var\$6) 5 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 4 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 4 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 5 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 6 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 7 "324"/\$.ccls. and sean adj chain with segment\$4 with fault 1 "714"/\$.ccls. and scan adj chain with segment\$4 with fault 1 "714"/\$.ccls. and scan adj chain with segment\$4 with fault 1 "714"/\$.ccls. and scan adj chain with segment\$4 with fault 1 "714"/\$.ccls. and scan adj chain with segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with (segment\$4 partition\$3) same	jitter\$3 and phase adj (offset adjust\$5 ver\$6) and data adj recover\$3 and integrated adj circuit "6331999".PN. "4809306".PN. "324"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 ver\$6) and data adj recover\$3 12 phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 ver\$6) and data adj recover\$3 12 phase adj lock\$3 adj loop with jitter\$3 and phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 ver\$6) and data adj recover\$3 and integrated adj circuit 8 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 3 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 5 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 4 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 4 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 5 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 6 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 7 "324"/\$.ccls. and scan adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 8 "324"/\$.ccls. and scan adj chain (uSPAT) 800 "714"/\$.ccls. and scan adj chain with segment\$4 with fault 1 "714"/\$.ccls. and scan adj chain with segment\$4 with fault 2 "714"/\$.ccls. and scan adj chain with segment\$4 with fault 1 "714"/\$.ccls. and scan adj chain with segment\$4 with fault 10 "714"/\$.ccls. and scan adj chain with segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with (segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with (segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with (segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with (segment\$4 same fault	jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 and integrated adj circuit DERWENT 1 "4809306".PN. USPAT; USOCR 1 "324"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 12 phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 12 phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) and data adj recover\$3 13 "324"/\$.ccls. and phase adj lock\$3 adj loop with jitter\$3 and phase adj lock\$3 adj loop with jitter\$3 and phase adj (offset adjust\$5 var\$6) 3 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 with phase adj (offset adjust\$5 var\$6) 5 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 4 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 4 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 5 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 6 "324"/\$.ccls. and phase adj lock\$3 adj loop and jitter\$3 and phase adj (offset adjust\$5 var\$6) 7 phase with loop with recovery with jitter\$3 and integrated adj circuit 8 "324"/\$.ccls. and scan adj chain with segment\$4 with fault 1 "714"/\$.ccls. and scan adj chain with segment\$4 with fault 1 "714"/\$.ccls. and scan adj chain with segment\$4 with fault 10 "714"/\$.ccls. and scan adj chain with segment\$4 with fault 10 "714"/\$.ccls. and scan adj chain with segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with (segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with (segment\$4 same fault 9 "714"/\$.ccls. and scan adj chain with (segment\$4 same fault

S65	8	"714"/\$.ccls. and scan adj chain with (segment\$4 partition\$3) same fault	USPAT	OR	ON	2006/04/03 17:01
S16	2094	phase adj lock\$3 adj loop with jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:19
S70	14	S67 and S68	USPAT	OR	OFF	2006/09/28 10:20
S69	500	loop near3 recover\$3 near3 tim\$3	USPAT	OR	OFF	2006/09/28 10:20
S68	429	loop near3 recover\$3 near tim\$3	USPAT	OR	OFF	2006/09/28 10:32
S67	2228	phase adj lock\$3 adj loop with jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:32
S71		phase adj lock\$3 adj loop near2 recover\$3 near3 tim\$3 with jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:34
S72	6	phase adj lock\$3 adj loop near3 recover\$3 near3 tim\$3 with jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:36
S75	112	phase adj lock\$3 adj loop near3 tim\$3 with measur\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:37
S77	2	"20060126710"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 10:50
S76	31	phase adj lock\$3 adj loop near3 tim\$3 near2 measur\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:07
S78	10	phase adj lock\$3 adj loop near3 tim\$3 near2 measur\$5 and jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:10
S82	27244	data near1 recover\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:11
S81	39465	data near2 recover\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:11

S83	103	data near1 recover\$3 with tim\$3 with jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:12
S84	14	data near1 recover\$3 near3 tim\$3 near3 jitter\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:13
S85	12	data near1 recover\$3 near3 tim\$3 near3 measur\$5	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/09/28 11:14
S87	5	S86 and jitter\$3	US-PGPUB; USPAT	OR	OFF	2006/09/28 11:16
S88	1	"20060126710"	US-PGPUB; USPAT	OR	OFF	2006/09/28 11:40
S86	7	(US-20060126710-\$).did. or (US-6522122-\$ or US-6885209-\$ or US-6441602-\$ or US-6249557-\$ or US-5737369-\$ or US-6807498-\$). did.	US-PGPUB; USPAT	OR	OFF	2006/09/28 11:40